

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Yoshihisa FUJIMOTO Conf.:
Appl. No.: NEW Group:
Filed: December 2, 2003 Examiner:
For: SWITCHED CAPACITOR CIRCUIT

INFORMATION DISCLOSURE STATEMENT
(SUBMISSION WITH CONTINUATION-IN-PART OR
RULE 1.53(b) CONTINUATION OR DIVISIONAL APPLICATION)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 2, 2003

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98, applicant(s) hereby submit(s) an Information Disclosure Statement for consideration by the Examiner.

I. LIST OF PATENTS, PUBLICATIONS OR OTHER INFORMATION

The patents, publications, or other information submitted for consideration by the Office are listed on the PTO-1449 form(s), attached hereto.

II. REFERENCES PREVIOUSLY CITED OR SUBMITTED

Pursuant to 37 C.F.R. § 1.98(d), consideration of information listed on the PTO-1449 form(s) is requested since any patents, publications, or other information which are listed on the PTO-1449 form(s) but for which copies are not enclosed herewith, were previously cited by or submitted to the PTO in one of the following applications which has been relied upon for an earlier filing date under 35 U.S.C. § 120:

U.S. Appl. No(s).
10/207,177

U.S. Filing Date(s)
July 30, 2002

III. FEES

This Information Disclosure Statement is being filed concurrent with the filing of a continuation-in-part, continuation, or divisional patent application; therefore, no fee is required.

If the Examiner has any questions concerning this IDS or requires a copy of any of the references cited but not provided, he/she is requested to contact the undersigned. If it is determined that this IDS has been filed under the wrong rule, the PTO is requested to consider this IDS under the proper rule and charge the appropriate fee to Deposit Account No. 02-2448.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.


Respectfully submitted,

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Attachment(s): ☒ PTO-1449(s)

☐ References

☐ Foreign Search Report

☐ Other:

(Rev. 09/30/03)

(Use several sheets if necessary)

APPLICATION NO.
NEW

APPLICANT
Yoshihisa FUJIMOTO

FILING DATE
December 2, 2003

GROUP	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS

OTHER DOCUMENTS (Include Name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.

"ANALOG MOS INTEGRATED CIRCUITS FOR SIGNAL PROCESSING", R. Gregorian, et al., published 1986, pages 513-524.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.